REMARKS

Claims 1-14 were previously pending in this patent application. Claims 1-14 stand rejected. Herein, Claims 1, 5, and 10 have been amended. Accordingly, after this Amendment and Response After Final Action, Claims 1-14 remain pending in this patent application. Further examination and reconsideration in view of the claims, remarks, and arguments set forth below is respectfully requested.

35 U.S.C. Section 112, first paragraph, Rejections

Claims 1-14 stand rejected under 35 U.S.C. Section 112, first paragraph. The amendments to the claims overcome the rejections under 35 U.S.C. Section 112, first paragraph. In particular, Independent Claim 1 has been amended (and similar amendments have been made to Claim 5 and 10) to recite, "wherein **selection of** said portion of said particular address **for said concatenating** is independent of region of said memory unit from which said instruction is fetched," (emphasis added). This is supported by the following passages (page 4, lines 2-4 and page 6, lines 13-14) of the Specification: "Rather than increasing an instruction set by increasing the size of the instruction 10, the instruction set is increased by utilizing one or more bits from the corresponding address 20 of the instruction 10 in memory," and, "As described above, any portion of the address can be concatenated to the instruction to determine its meaning."

35 U.S.C. Section 102(b) Rejections

Claims 1-3, 5-7, and 10-12 stand rejected under 35 U.S.C. 102(b) as being anticipated by Larsen, U.S. Patent No. 5,115,500 (hereafter Larsen). These rejections are respectfully traversed.

Independent Claim 1 recites (as amended):

A method of processing an instruction, said method comprising:
 fetching said instruction using a corresponding address from a memory unit, wherein a plurality of possible meanings are associated with said instruction;
 concatenating a portion of said corresponding address to said instruction to form an extended instruction, wherein ***selection of said portion of said corresponding address for said concatenating is independent of region of said memory unit from which said instruction is fetched***; and
 executing said extended instruction, wherein said portion of said corresponding address determines a meaning for said extended instruction from said possible meanings. (emphasis added)

It is respectfully asserted that Larsen does not disclose the present invention as recited in Independent Claim 1. In particular, Independent Claim 1 recites the limitation, "***concatenating a portion*** of said corresponding address to said instruction to form an extended instruction, wherein ***selection of said portion of said corresponding address for said concatenating is independent of region of said memory unit from which said instruction is fetched***," (emphasis added). In contrast, Larsen discloses an instruction store (2) with partitioning for accommodating two types of different, incompatible format machine language instructions. [Larsen; Figure 2; Col. 5, lines 34-51]. That is, selection of the portion of the address to be concatenated with the

instruction <u>is dependent on the region of instruction store (2) from which the instruction is fetched</u>. Moreover, if 1/8 of the instruction store total capacity is reserved for type 1 instructions and 7/8 of the instruction store total capacity is reserved for type 2 instructions, the <u>high order three bits</u> of the address <u>must</u> be concatenated with the instruction to know whether the instruction is fetched from a region storing type 1 instructions or from a region storing type 2 instructions. If other bits of the address are selected for concatenation, Larsen would be inoperable. Thus, Larsen <u>fails</u> to show <u>concatenating a portion</u> of the corresponding address to the instruction to form an extended instruction, wherein <u>selection of the portion</u> of the corresponding address for the concatenating <u>is independent of region of the memory unit from which the instruction is fetched</u>, as in the invention of Independent Claim 1. Therefore, it is respectfully submitted that Independent Claim 1 is not anticipated by Larsen and is in condition for allowance.

Dependent Claims 2-3 are dependent on allowable Independent Claim 1, which is allowable over Larsen. Hence, it is respectfully submitted that Dependent Claims 2-3 are patentable over Larsen for the reasons discussed above.

With respect to Independent Claims 5 and 10, it is respectfully submitted that Independent Claims 5 and 10 recite similar limitations as in Independent Claim 1. In particular, Independent Claim 5 recites, "...***concatenating said***

portion of said particular address to said instruction, wherein *selection of* said portion of said particular address *for said concatenating is independent of region of said memory unit from which said instruction is fetched*," (emphasis added). Moreover, Independent Claim 10 recites, "...operable to *concatenate a portion of said corresponding* address to said particular instruction to determine a meaning for said particular instruction from said possible meanings before executing said particular instruction, wherein *selection of* said portion of said corresponding address *for said concatenation is independent of region of said memory unit from which said instruction is fetched*," (emphasis added). As discussed above, Larsen <u>fails</u> to disclose the cited limitations of the inventions of Independent Claims 5 and 10. Therefore, Independent Claims 5 and 10 are not anticipated by Larsen and are in condition for allowance for reasons discussed in connection with Independent Claim 1.

Dependent Claims 6-7 and Dependent Claims 11-12 are dependent on allowable Independent Claims 5 and 10, respectively, which are allowable over Larsen. Hence, it is respectfully submitted that Dependent Claims 6-7 and 11-12 are patentable over Larsen for the reasons discussed above.

35 U.S.C. Section 103(a) Rejections

Claims 4, 8-9, and 13-14 stand rejected under 35 U.S.C. 103(a) as being unpatentable over Larsen, U.S. Patent No. 5,115,500 (hereafter Larsen) and Official Notice. These rejections are respectfully traversed.

Dependent Claims 4, Dependent Claim 8-9, and Dependent Claims 13-14 are dependent on allowable Independent Claims 1, 5, and 10, respectively, which are allowable over Larsen. Moreover, the Official Notice <u>fails</u> to disclose <u>concatenating a portion</u> of the corresponding address to the instruction to form an extended instruction, wherein <u>selection of</u> the portion of the corresponding address <u>for concatenating is independent of region of the memory unit from which the instruction is fetched</u>, as in the inventions of Independent Claims 1, 5, and 10. Therefore, Independent Claims 1, 5, and 10 are allowable over Larsen and Official Notice for the reasons discussed above. Since Dependent Claims 4, Dependent Claim 8-9, and Dependent Claims 13-14 depend from Independent Claims 1, 5, and 10, respectively, it is respectfully submitted that Dependent Claims 4, 8-9, and 13-14 are patentable over Larsen and Official Notice for the reasons discussed above.

CONCLUSION

It is respectfully submitted that the above claims, arguments, and remarks overcome all rejections. All remaining claims (Claims 1-14) are neither anticipated nor obvious in view of the cited references. For at least the above-presented reasons, it is respectfully submitted that all remaining claims (Claims 1-14) are in condition for allowance.
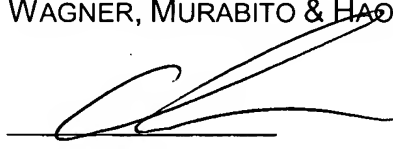
The Examiner is urged to contact Applicants' undersigned representative if the Examiner believes such action would expedite resolution of the present Application.

Please charge any additional fees or apply any credits to our PTO deposit account number: 23-0085.

Respectfully submitted,

WAGNER, MURABITO & HAO, LLP

Dated: _Ay 31, 2005_

Anthony C. Murabito
Registration No. 35,295

Two North Market Street, Third Floor
San Jose, CA 95113
(408) 938-9060